

| Ref # | Hits | Search Query  | DBs                                      | Default Operator | Plurals | Time Stamp       |
|-------|------|---|--|------------------|---------|------------------|
| L1    | 2    | (parallel adj interface) and BSCAN                          | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:04 |
| L2    | 0    | (parallel adj interface) and (on adj board adj programming) | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:05 |
| L3    | 1307 | (parallel adj interface) and programming                    | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:05 |
| L4    | 68   | 3 and(memory adj cell\$1)                                   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:06 |
| L5    | 4    | 4 and (boundary adj scan)                                   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:07 |
| L6    | 16   | 4 and (data adj register)                                   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:07 |
| L7    | 12   | 6 and write   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:09 |
| L8    | 5099 | flash and ASIC  | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:09 |
| L9    | 2248 | 8 and write   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:09 |
| L10   | 16   | 9 and (boundary adj scan adj register)                      | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:12 |
| L11   | 622  | 8 and (memory adj cell\$1)                                  | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:12 |
| L12   | 30   | 11 and (boundary adj scan)                                  | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR               | ON      | 2005/04/13 12:28 |

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|-----|-----|---|--|----|----|------------------|
| L13 | 487 | (parallel adj interface) and ASIC                   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:28 |
| L14 | 418 | 13 and program\$3                                   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:28 |
| L15 | 0   | 14 and (mmeory adj cell\$1)                         | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:29 |
| L16 | 14  | 14 and (memory adj cell\$1)                         | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:31 |
| L17 | 170 | 14 and (data adj register)                          | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:31 |
| L18 | 118 | 17 and series                                       | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:31 |
| L19 | 61  | 18 and (system adj configuration)                   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:38 |
| L20 | 282 | (boundary adj scan adj register)<br>and multiplexer | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:38 |
| L21 | 187 | 20 and (flip adj flop)                              | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:38 |
| L22 | 131 | 21 and capture                                      | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:39 |
| L23 | 53  | 22 and ASIC   | USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT | OR | ON | 2005/04/13 12:39 |